TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

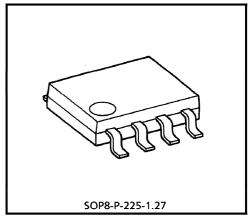
# TD7101F

# **ELC PRESCALER FOR DIGITAL SYNTHESIZED TUNER**

TD7101F is a 2 modulus prescaler developed for low operating voltage digital synthesized tuner, and can operate up to 150 MHz.

#### **FEATURES**

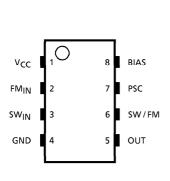
- Operating frequency range is 1.5~35 MHz/50~150 MHz.
- 2 modulus prescaler :  $N = 4 \times 15/16$  and N = 15/16
- Input voltage sensitivity is V<sub>IN</sub> (FM) = 35 mV<sub>rms</sub>, V<sub>IN</sub> (SW)
   = 40 mV<sub>rms</sub>
- 3 V low operating supply voltage.
- The package is SOP-8 pins.

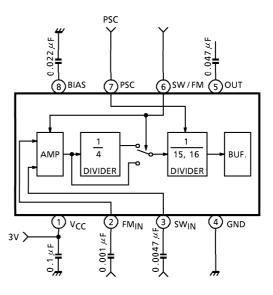


Weight: 0.08 g (Typ.)

#### PIN CONNECTION

### **BLOCK DIAGRAM**





(Note) This device is vulnerable to surge voltage. Take it into account when using this device in your system.

## PIN FUNCTION

PIN No.	SYMBOL	FUNCTION	REMARKS		
1	Vcc	Power supply terminal.	_		
2	FMIN	Signal input terminal from FM local oscillator.	_		
3	SWIN	Signal input terminal from SW local oscillator.	_		
4	GND	Ground terminal.	_		
5	OUT	Divider signal output terminal.	_		
6	SW / FM	Dividing mode control terminal.  "H" level input: SW <sub>IN</sub> is selected, direct mode.  "L" level input: FM <sub>IN</sub> is selected, 1/4 mode.	_		
7	PSC	2 modulus mode control terminal. "H" level input: 1/16 dividing "L" level input: 1/15 dividing	_		
8	BIAS	Bias capacitor terminal. Bias capacitor is connected.	_		

# **MAXIMUM RATINGS** (Ta = 25°C)

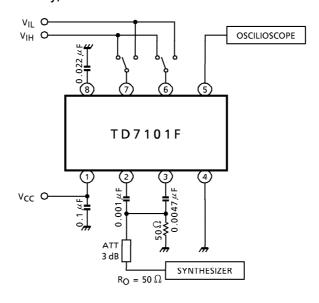
CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	VCC	6.5	٧
Power Dissipation	PD	200	mW
Input Voltage	VIN	-0.3~V <sub>CC</sub> + 0.3	٧
Operating Temperature	Topr	- 10~60	°C
Storage Temperature	T <sub>stg</sub>	<b>-</b> 55∼150	°C

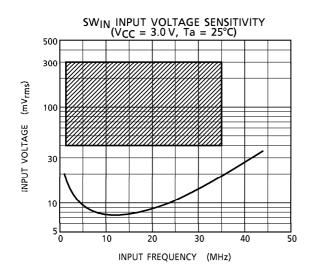
### **ELECTRICAL CHARACTERISTICS**

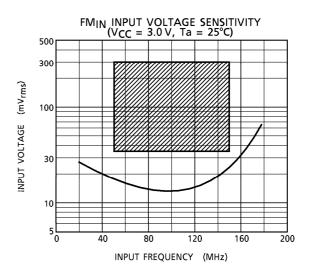
(Unless otherwise specified,  $V_{CC} = 1.8 \sim 5.5 \text{ V}$ ,  $T_a = -10 \sim 60 ^{\circ}\text{C}$ ,  $f_{in}$  (FM) =  $50 \sim 150 \text{ MHz}$ ,  $f_{in}$  (SW) =  $1.5 \sim 35 \text{ MHz}$ )

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage		V <sub>C</sub> C	_	_	1.8	3.0	5.5	V
Supply Cuurent		Icc	_	V <sub>CC</sub> = 3.0 V	_	5.5	9.5	mA
Operating Frequency		fin 1	_	FMIN	50	_	150	MHz
Range		f <sub>IN 2</sub>	_	sw <sub>IN</sub>	1.5	_	35	
Input Voltage Range		V <sub>IN 1</sub>	_	FMIN	35	_	300	mV <sub>rms</sub>
		V <sub>IN 2</sub>	_	sw <sub>IN</sub>	40	_	300	
Output Amplitude		Vout	_	_	0.5	_	_	V <sub>p-p</sub>
Input Voltage	"H" Level	VIH	_	PSC, SW/FM	1.6	_	Vcc	V
	"L" Level	V <sub>IL</sub>	_	PSC, SW/FM	0	_	1.0	
Input Current	"H" Level	'H" Level I <sub>IH</sub>	_	PSC, SW/FM,	_	_	60	μΑ
				V <sub>CC</sub> = 5.0 V, V <sub>IH</sub> = 4.0 V				
	"L" Level	" Level   I <sub>I</sub> L	_	PSC, SW/FM,	_	_	10	
				$V_{CC} = 5.0 \text{ V}, V_{IL} = 1.0 \text{ V}$				

# TEST CIRCUIT (Input voltage sensitivity)







(Note)

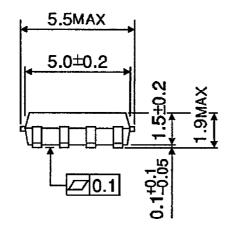
Operating range ( $V_{CC} = 1.8 \sim 5.5 \text{ V}$ , Ta =  $-10 \sim 60 ^{\circ}\text{C}$ )

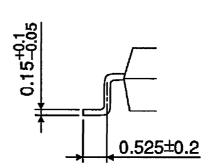
### **PACKAGE DIMENSIONS**

SOP8-P-225-1.27 Unit : mm

8
5
6
7
7
8
7
8
7
7
9
1.27

Unit : mm





Weight: 0.08 g (Typ.)

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000707EBA

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