

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA8276H

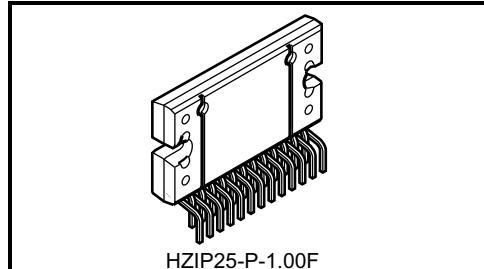
Max Power 35 W BTL × 4 ch Audio Power IC

The TA8276H is 4 ch BTL audio power amplifier for car audio application.

This IC can generate more high power: POUTMAX = 35 W as it is included the pure complementary PNP and NPN transistor output stage.

It is designed low distortion ratio for 4 ch BTL audio power amplifier, built-in stand-by function, muting function, output clipping detection and diagnosis circuit which can detect output to VCC/GND short and over voltage input mode.

Additionally, the AUX amplifier and various kind of protector for car audio use are built-in.

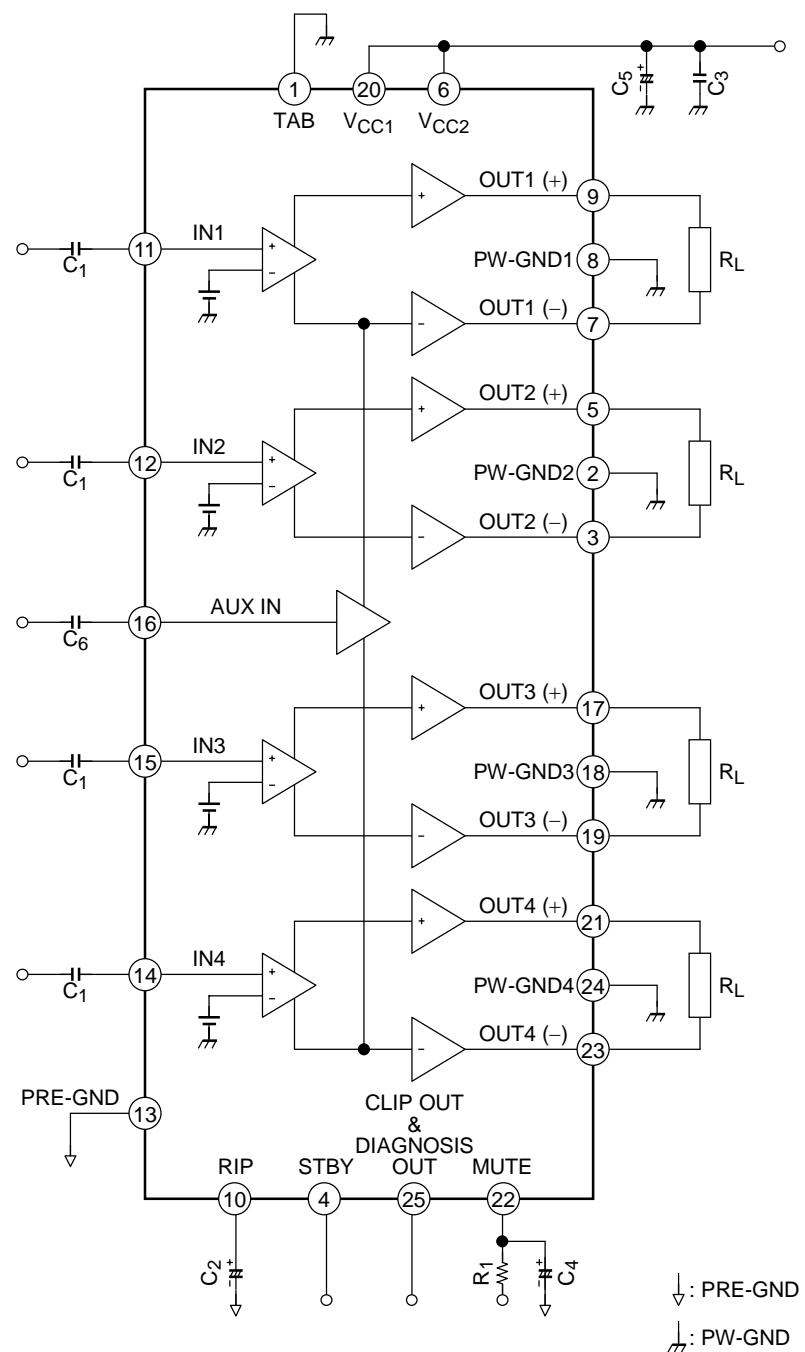


Weight: 7.7 g (typ.)

## Features

- High power: POUTMAX (1) = 35 W (typ.)  
(VCC = 14.4 V, f = 1 kHz, EIAJ max, RL = 4 Ω)  
: POUTMAX (2) = 31 W (typ.)  
(VCC = 13.7 V, f = 1 kHz, EIAJ max, RL = 4 Ω)  
: POUT (1) = 23 W (typ.)  
(VCC = 14.4 V, f = 1 kHz, THD = 10%, RL = 4 Ω)  
: POUT (2) = 20 W (typ.)  
(VCC = 13.2 V, f = 1 kHz, THD = 10%, RL = 4 Ω)
- Built-in output clipping detection and diagnosis circuit (pin 25)
- Low distortion ratio: THD = 0.02% (typ.)  
(VCC = 13.2 V, f = 1 kHz, POUT = 5 W, RL = 4 Ω)
- Low noise: VNO = 0.10 mVrms (typ.)  
(VCC = 13.2 V, Rg = 0 Ω, GV = 26 dB, BW = 20 Hz~20 kHz)
- Built-in stand-by switch (pin 4)
- Built-in muting function (pin 22)
- Built-in AUX amplifier from single input to 2 channels output (pin 16)
- Built-in various protection circuit  
: Thermal shut down, over voltage, out to GND, out to VCC, out to out short, speaker burned
- Operating supply voltage: VCC (opr) = 9~18 V

## Block Diagram



**Maximum Ratings (Ta = 25°C)**

| Characteristics             | Symbol                  | Rating  | Unit |
|-----------------------------|-------------------------|---------|------|
| Peak supply voltage (0.2 s) | V <sub>CC</sub> (surge) | 50      | V    |
| DC supply voltage           | V <sub>CC</sub> (DC)    | 25      | V    |
| Operation supply voltage    | V <sub>CC</sub> (opr)   | 18      | V    |
| Output current (peak)       | I <sub>O</sub> (peak)   | 9       | A    |
| Power dissipation           | P <sub>D</sub> (Note 1) | 125     | W    |
| Operation temperature       | T <sub>opr</sub>        | -40~85  | °C   |
| Storage temperature         | T <sub>stg</sub>        | -55~150 | °C   |

Note 1: Package thermal resistance  $\theta_{j-T} = 1^{\circ}\text{C}/\text{W}$  (typ.)

(Ta = 25°C, with infinite heat sink)

**Electrical Characteristics**

(unless otherwise specified, V<sub>CC</sub> = 13.2 V, f = 1 kHz, R<sub>L</sub> = 4 Ω, Ta = 25°C)

| Characteristics                  | Symbol                   | Test Circuit | Test Condition   | Min  | Typ. | Max             | Unit              |
|----------------------------------|--------------------------|--------------|--|------|------|-----------------|-------------------|
| Quiescent current                | I <sub>CCQ</sub>         | —            | V <sub>IN</sub> = 0  | —    | 200  | 400             | mA                |
| Output power                     | P <sub>OUT</sub> MAX (1) | —            | V <sub>CC</sub> = 14.4 V, max Power  | —    | 35   | —               | W                 |
|                                  | P <sub>OUT</sub> MAX (2) | —            | V <sub>CC</sub> = 13.7 V, max Power  | —    | 31   | —               |                   |
|                                  | P <sub>OUT</sub> (1)     | —            | V <sub>CC</sub> = 14.4 V, THD = 10%  | —    | 23   | —               |                   |
|                                  | P <sub>OUT</sub> (2)     | —            | THD = 10%  | 17   | 20   | —               |                   |
| Total harmonic distortion        | THD                      | —            | P <sub>OUT</sub> = 5 W   | —    | 0.02 | 0.2             | %                 |
| Voltage gain                     | G <sub>V</sub>           | —            | V <sub>OUT</sub> = 0.775 V <sub>rms</sub> (0 dBm)  | 24   | 26   | 28              | dB                |
| Voltage gain ratio               | ΔG <sub>V</sub>          | —            | V <sub>OUT</sub> = 0.775 V <sub>rms</sub> (0 dBm)  | -1.0 | 0    | 1.0             |                   |
| Output noise voltage             | V <sub>NO</sub> (1)      | —            | R <sub>g</sub> = 0 Ω, DIN45405   | —    | 0.12 | —               | mV <sub>rms</sub> |
|                                  | V <sub>NO</sub> (2)      | —            | R <sub>g</sub> = 0 Ω, BW = 20 Hz~20 kHz  | —    | 0.10 | 0.35            |                   |
| Ripple rejection ratio           | R.R.                     | —            | f <sub>rip</sub> = 100 Hz, R <sub>g</sub> = 620 Ω<br>V <sub>rip</sub> = 0.775 V <sub>rms</sub> (0 dBm) | 40   | 50   | —               | dB                |
| Cross talk                       | C.T.                     | —            | R <sub>g</sub> = 620 Ω<br>V <sub>OUT</sub> = 0.775 V <sub>rms</sub> (0 dBm)                            | —    | 65   | —               | dB                |
| Output offset voltage            | V <sub>OFFSET</sub>      | —            | —  | -150 | 0    | 150             | mV                |
| Input resistance                 | R <sub>IN</sub>          | —            | —  | —    | 90   | —               | kΩ                |
| Stand-by current                 | I <sub>SB</sub>          | —            | Stand-by condition   | —    | 2    | 10              | μA                |
| Stand-by control voltage         | V <sub>SB</sub> H        | —            | Power: ON  | 3.0  | —    | V <sub>CC</sub> | V                 |
|                                  | V <sub>SB</sub> L        | —            | Power: OFF   | 0    | —    | 1.5             |                   |
| Mute control voltage<br>(Note 2) | V <sub>M</sub> H         | —            | Mute: OFF  | Open |      |                 | —                 |
|                                  | V <sub>M</sub> L         | —            | Mute: ON, R <sub>1</sub> = 10 kΩ   | 0    | —    | 0.5             | V                 |
| Mute attenuation                 | ATT M                    | —            | Mute: ON,<br>V <sub>OUT</sub> = 7.75 V <sub>rms</sub> (20 dBm) at<br>Mute: OFF.                        | 80   | 90   | —               | dB                |

Note 2: Muting function have to be controlled by open and low logic, which logic is a transistor, FET and μ-COM port of I<sub>MUTE</sub> > 250 μA ability.

This means than the mute control terminal : pin 22 must not be pulled-up.

## Test Circuit

